

09/344847

Gajinder Panesar

EAST SEARCH

6/27/02

L#	Hits	Search String	Databases
L2	29544	ASP or ASIP	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L5	29449	ASP	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L6	102	ASIP	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L7	6355	2 and peripheral\$	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L8	29546	1 or 2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L9	6355	8 and peripheral\$	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L10	11	9 and "input file"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L13	205	9 and (peripheral with (functions or functional))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L15	1	9 and "register definition"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L14	2	10 and 13	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L11	2	9 and "input data structure"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L16	3	5,557,774.pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L19	3	16 and hardware	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L20	1	8 and "register definition"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L23	202	(Application or application)adj specific adj (processor or hardware)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L24	46	23 and peripheral\$	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L25	2	24 and "input file"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L27	2	23 and "register definition"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L26	14	24 and "input data"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L28	2	26 and 27	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L29	16221	ASIC	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L30	3956	29 and peripheral\$	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L31	20	30 and "register definition"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L32	14	31 and "input data"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L33	1635	29 and (ASIC with design)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB
L34	9	33 and "register definition"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB

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Results of search set L32: ASIC and peripheral\$ and "register definition" and "input data"

Document ID

Title

Source

Issue Date Current OR

US 6389379 B1	Converfication system and method	20020514 703/14
US 6321366 B1	Timing-insensitive glitch-free logic system and method	20011120 716/6
US 6314416 B1	Reconfigurable expert rule processing system	20011106 706/60
US 6222840 B1	Method and system for performing concurrent read and write cycles in network switch	20010424 370/389
US 6212199 B1	Apparatus and method for interpretation and translation of serial digital audio transmission formats	20010403 370/466
US 6201789 B1	Network switch with dynamic backpressure per port	20010313 370/230
US 6134516 A	Simulation server system and method	20001017 703/27
US 6094434 A	Network switch with separate cut-through buffer	20000725 370/401
US 6026230 A	Memory simulation system and method	20000215 703/13
US 6009256 A	Simulation/emulation system and method	19991228 703/13
US 5862338 A	Polling system that determines the status of network ports and that stores values indicative thereof	19990119 709/224
US 5557774 A	Method for making test environmental programs	19960917 703/21
US 5392406 A	DMA data path aligner and network adaptor utilizing same	19950221 710/316
US 5319754 A	Data transfer system between a computer and a host adapter using multiple arrays	19940607 710/52